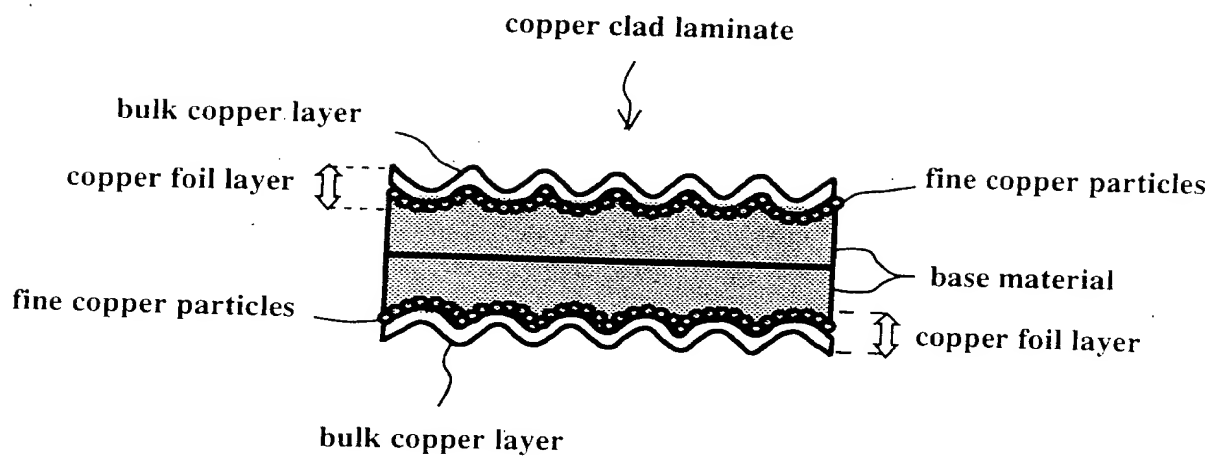




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FIG. 1



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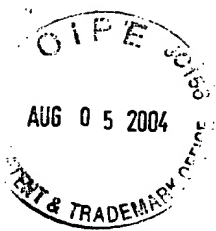
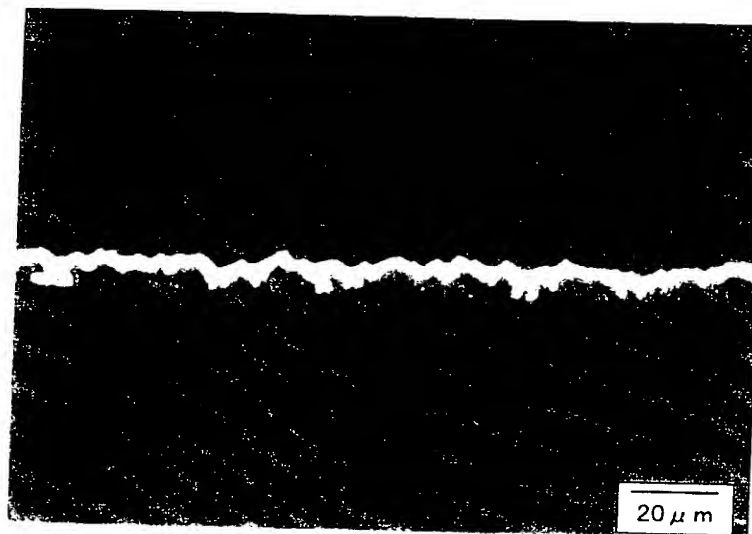


FIG. 2

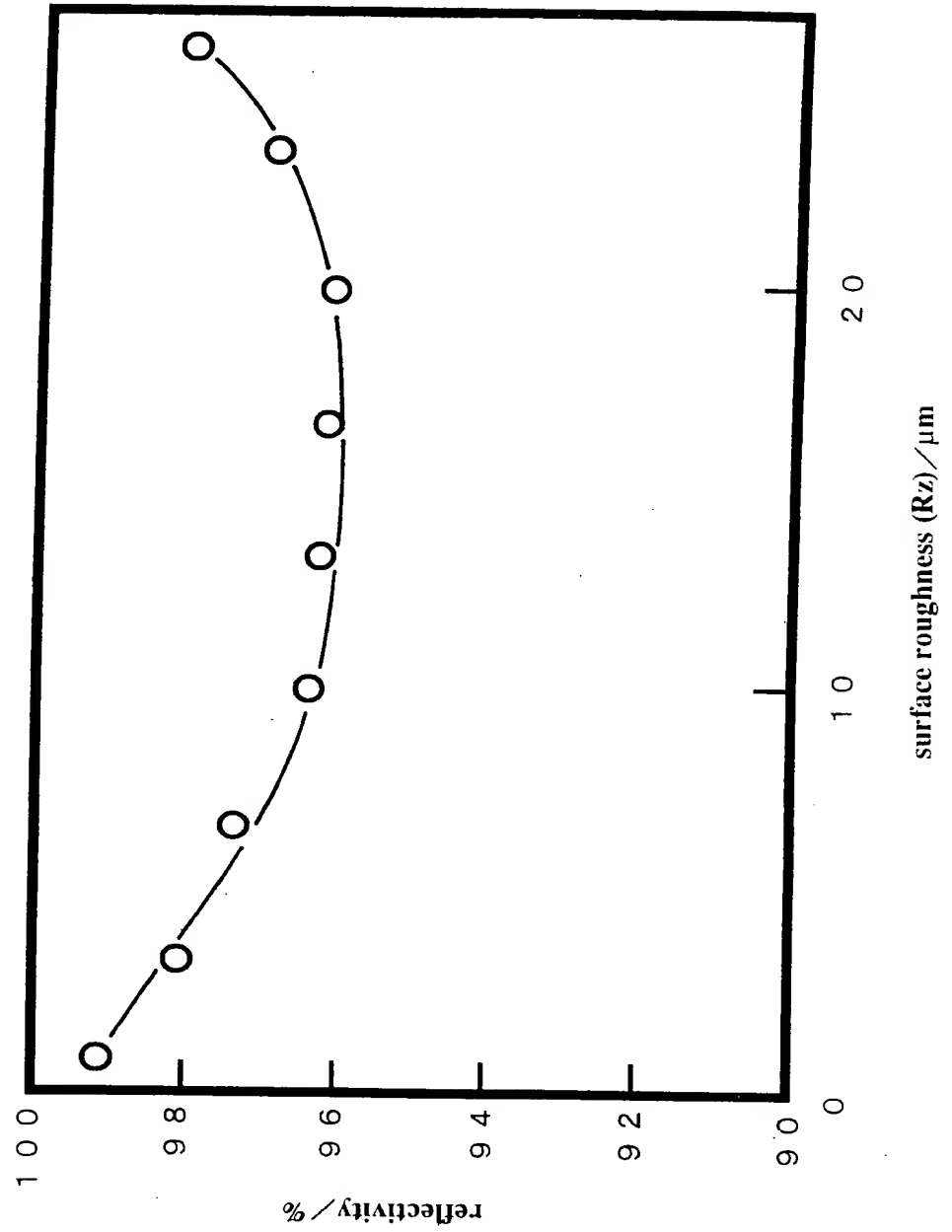


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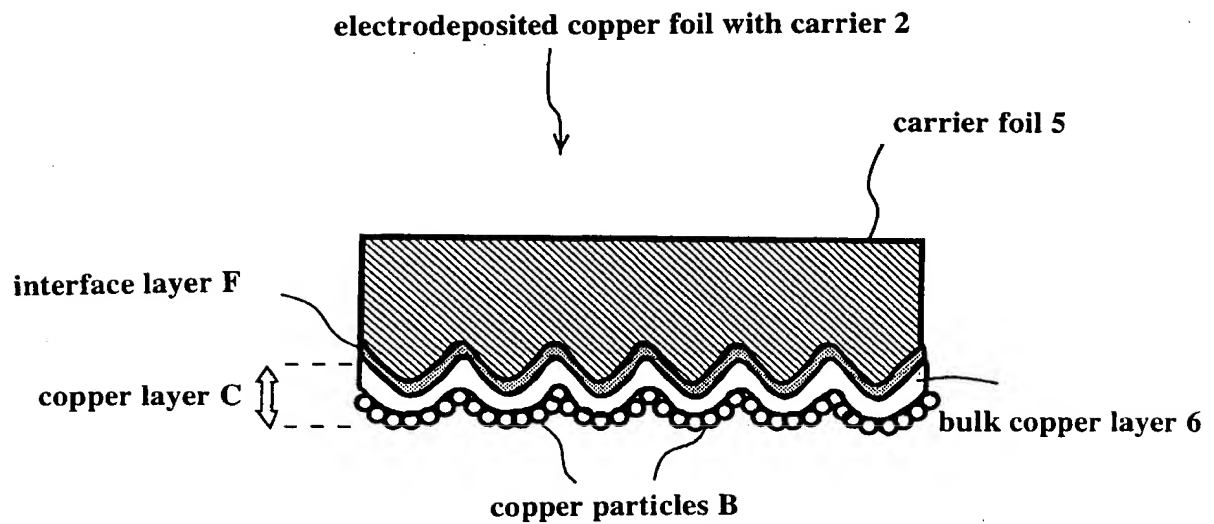
FIG. 3





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FIG. 4

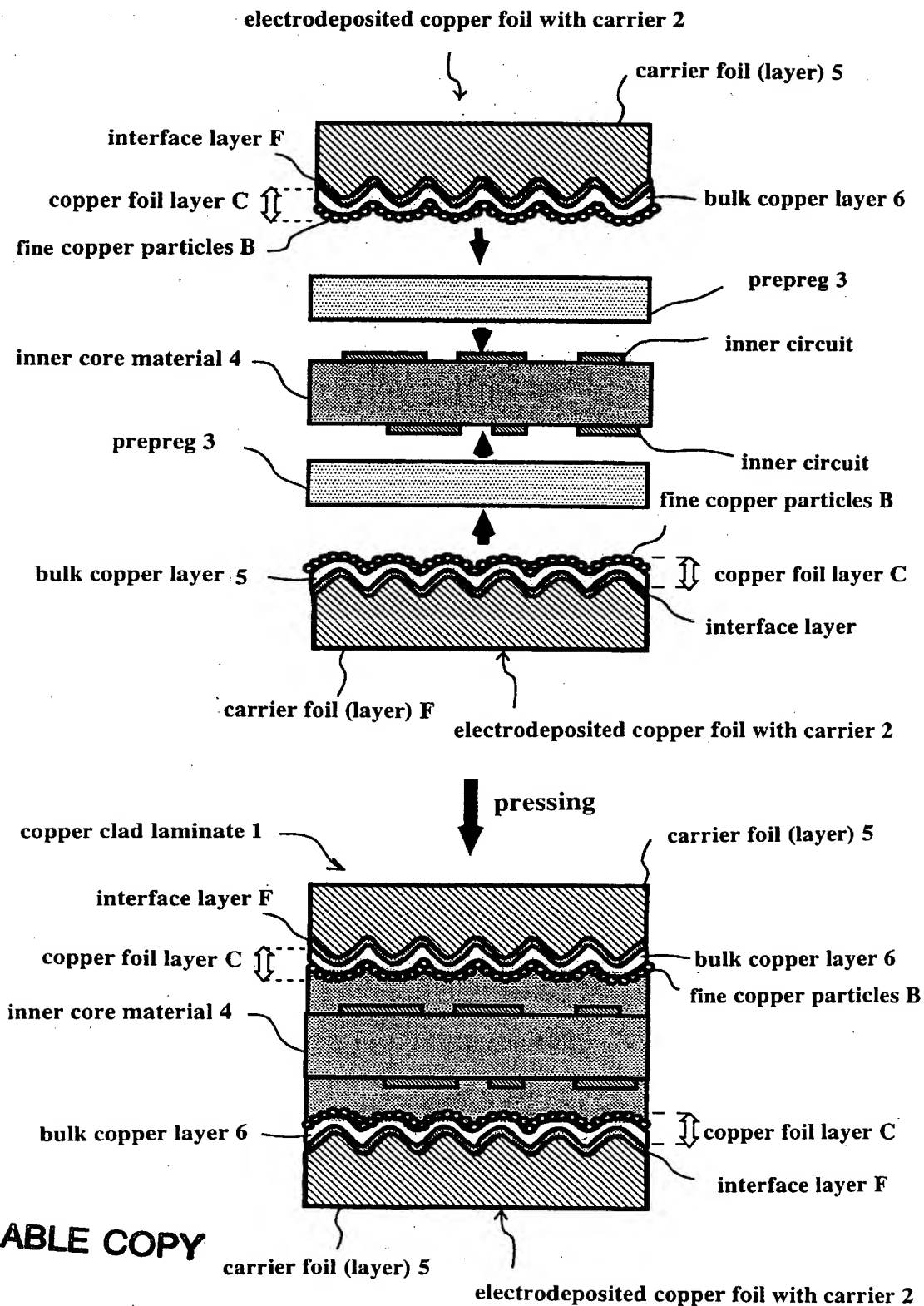


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FIG. 5A

(a) Manufacturing of copper clad laminate



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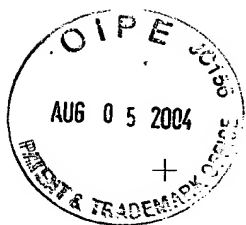


FIG. 5B

(b) Peeling of Carrier Foil

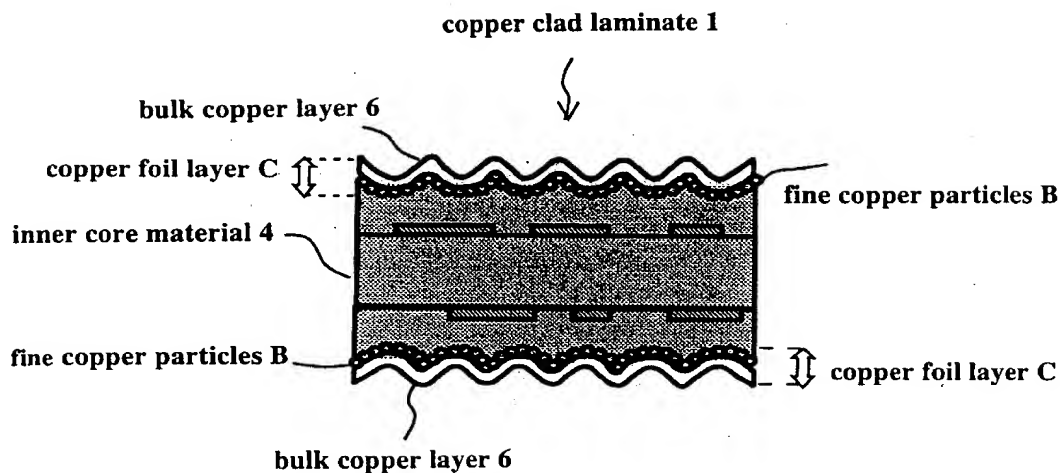
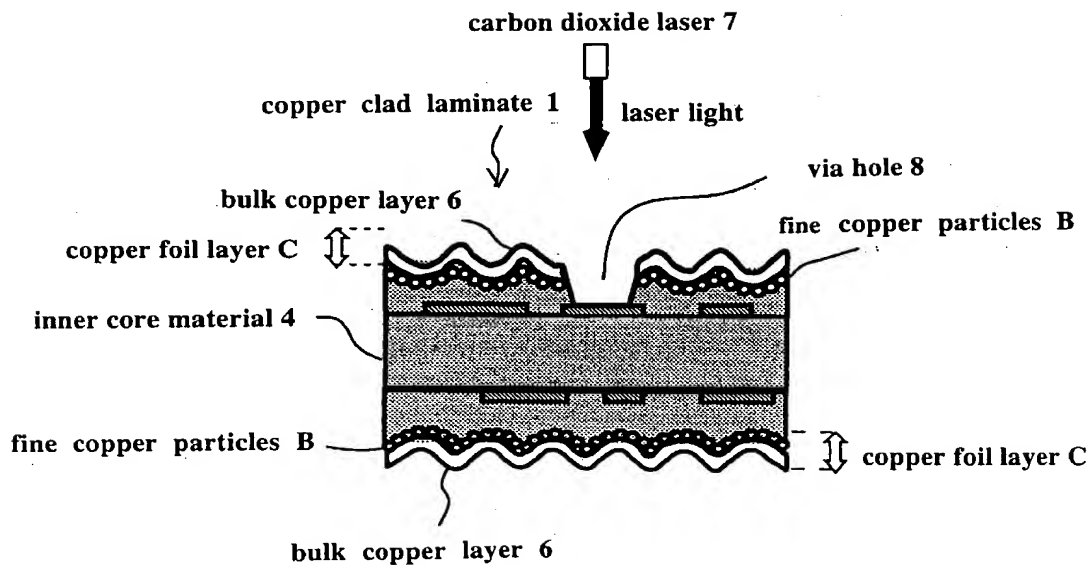


FIG. 5C

(c) Laser Treatment for Forming Holes



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FIG. 5D

(d) Copper Plating for Forming Interlayer Electrical Connection

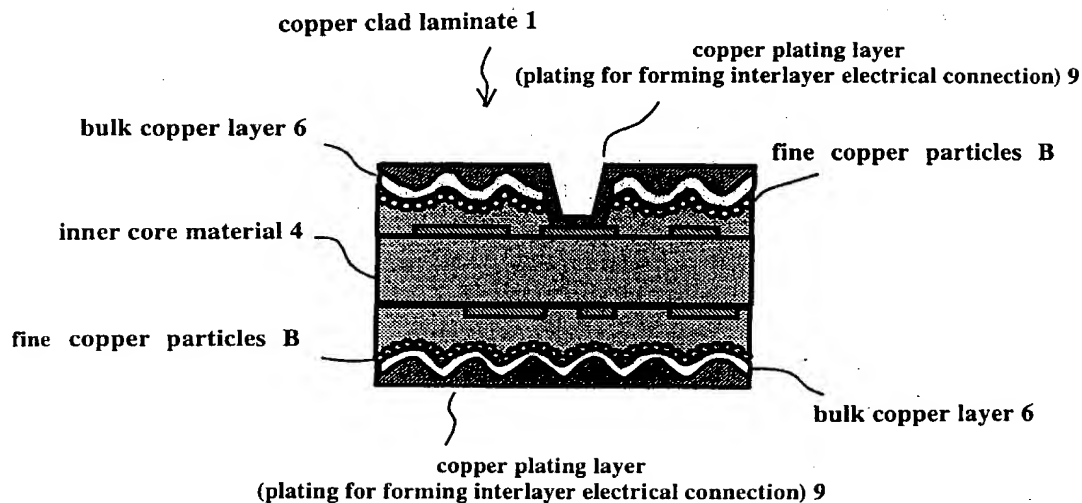
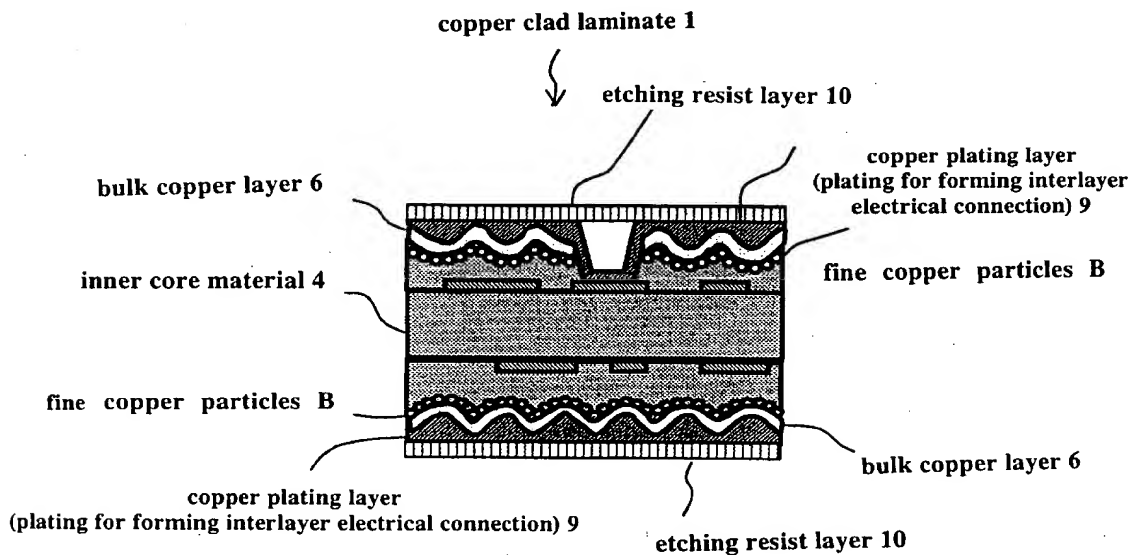


FIG. 5E

(e) Forming Etching Resist Layers



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FIG. 5F

(f) Exposing and Developing the Etching Resist Layers

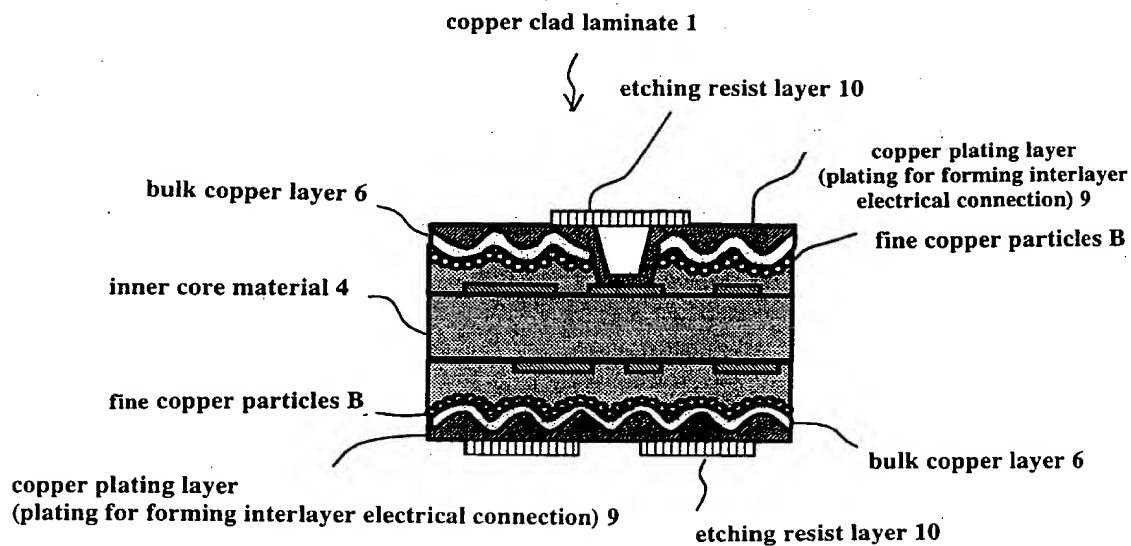
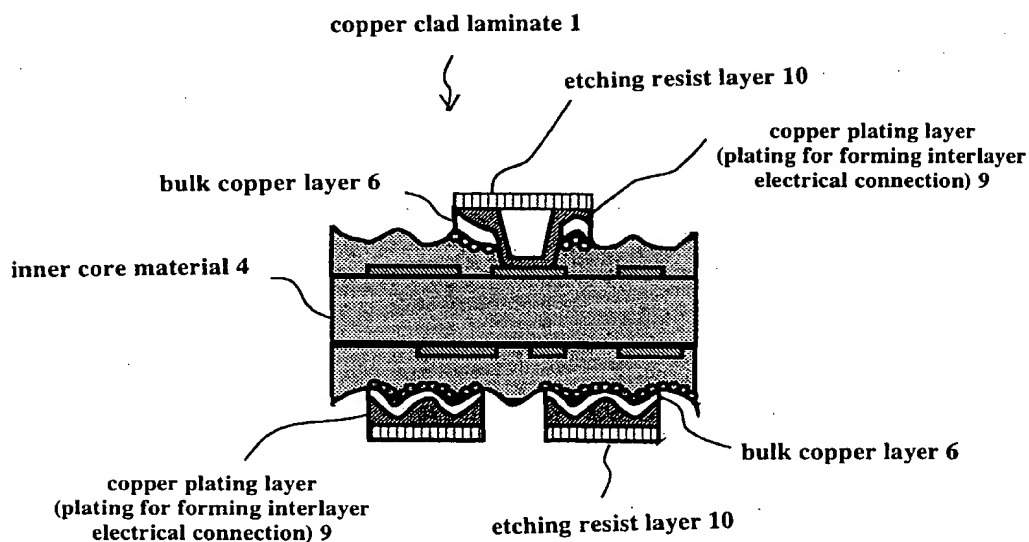


FIG. 5G

(g) Copper Etching



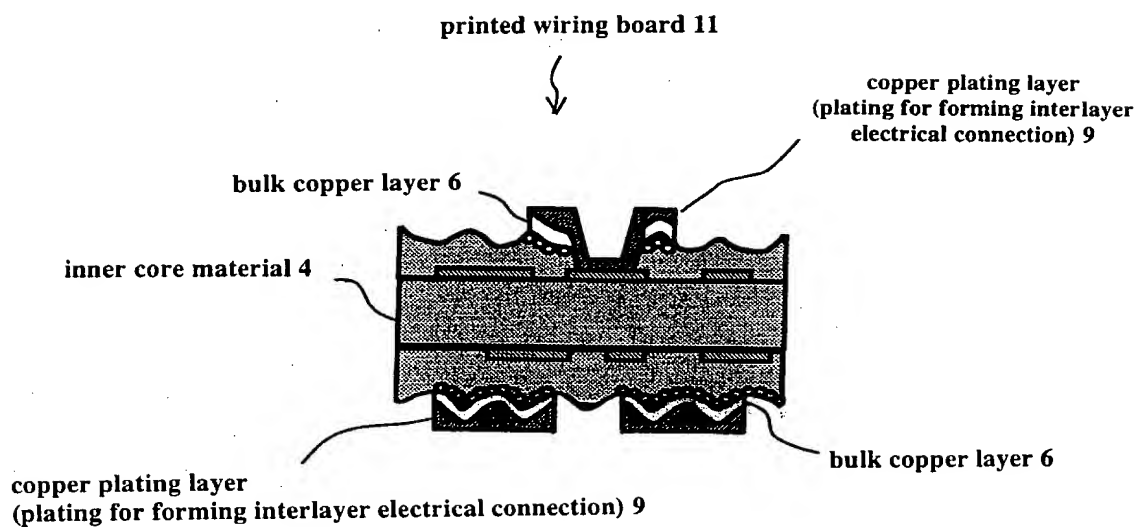
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FIG. 5H

(h) Peering of Etching Resist Layers



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